

CLMPTO
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CM.

1. **(Currently Amended)** A method for forming a first-property semiconductor film at a desired position on a substrate, comprising the steps of:
 - a) preparing the substrate having a second-property semiconductor film formed ~~thereon;~~ thereon in a gas atmosphere with a pressure of substantially atmospheric pressure;
 - b) preparing an optical mask having a predetermined pattern on another substrate which is apart from the substrate;
 - c) relatively positioning a projection area of the optical mask at the desired position on the substrate;
 - d) irradiating the desired position of the second-property semiconductor film with laser light through the optical mask to change an irradiated portion of the second-property semiconductor film to the first-property semiconductor film; and
 - e) forming an insulation film on the first-property semiconductor film and the second-property semiconductor film;

wherein the optical mask has an alignment mark pattern, wherein, in the step (d), an alignment mark corresponding to the alignment mark pattern is formed by the irradiation of the desired portion of the second-property semiconductor film with the laser light through the optical mask to change the irradiated portion of the second-property semiconductor film to the first-property semiconductor film, and wherein the alignment mark is visible due to a difference in optical characteristic between the first-property semiconductor film and the second-property semiconductor film.

CLAIMS 2-3. (CANCELLED)

4. **(Previously Presented)** The method according to claim 1, wherein a positioning process after the step (d) is performed with reference to the alignment mark.

CLAIM 5. (CANCELLED)

6. **(Previously Presented)** The method according to claim 1, wherein the first-property semiconductor film is a crystalline semiconductor film and the second-property semiconductor film is an amorphous semiconductor film.

CLAIM 7. (CANCELLED)

8. **(Currently Amended)** The method according to claim 7, 6, further comprising the step of:

- (f) forming an island comprised of the insulation film and the crystalline semiconductor film by a patterning process, wherein the crystalline semiconductor film of the island is a single-crystal semiconductor film used for source, drain, and channel regions of a field effect transistor.

CLAIM 9-10. (CANCELLED)

CLAIM 11-14. (CANCELLED)